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PATENT APPLICATION  
Docket No. 4591-342  
Client No. ID12015-US

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Mail Stop Patent Application  
Commissioner for Patents  
P.O. Box 1450  
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Enclosed for filing is a patent application under 37 CFR 1.53(b) of:

Inventor: Kyu-Chan Lee, Sang-Jae Rhee, Jung-Yong Choi, Jong-Hyun Choi,  
Jong-Sik Na, and Jae-Hoon Kim

For: SEMICONDUCTOR MEMORY DEVICE HAVING AN INTERNAL  
VOLTAGE GENERATION CIRCUIT FOR SELECTIVELY  
GENERATING AN INTERNAL VOLTAGE ACCORDING TO AN  
EXTERNAL VOLTAGE LEVEL

Applicant requests FIG. 3 to be published with the application.

Enclosures:

- ☒ Specification (pages 1-14); claims (pages 15-19); abstract (page 20)  
☒ FIFTEEN sheet(s) of FORMAL drawings (comprising 15 figures)

CLAIMS AS FILED				
For	Number Filed	Number Extra	Rate	Basic Fee \$750
Total Claims	18-20	0	x \$ 18 =	\$0.00
Independent Claims	7-3	5	x \$ 84 =	\$336.00
TOTAL FILING FEE				\$1086.00

☒ Combined Declaration and Power of Attorney

☒ Newly executed (original or copy)

☐ Assignment with cover sheet

Assignee Name and Address: Samsung Electronics Co.  
Suwon-City, Kyungki-do  
Republic of Korea

☒ Certified copy of Priority Document No. 2002-41543, filed July 16, 2002

☒ Return Postcard

☒ PTO Form 2038 authorizing credit card payment for the above-listed fees \$1086.00, filing fee (\$750.00) and extra claims fee (\$336.00) is enclosed.

☒ Any deficiency or overpayment should be charged or credited to deposit account number 13-1703.



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PATENT TRADEMARK OFFICE

Respectfully submitted,

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